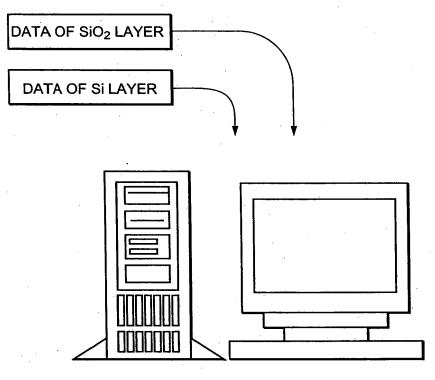


Fig.1



COMPUTER FOR SIMULATION

Fig.2

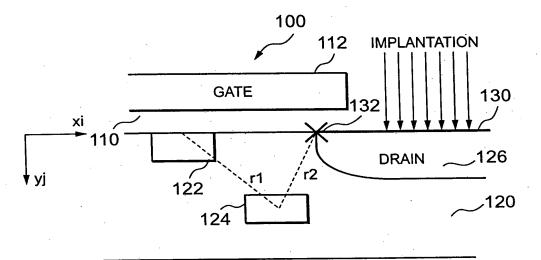


Fig.3

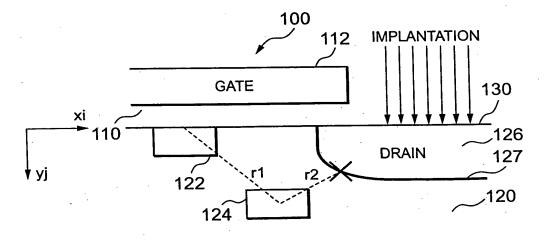


Fig.4

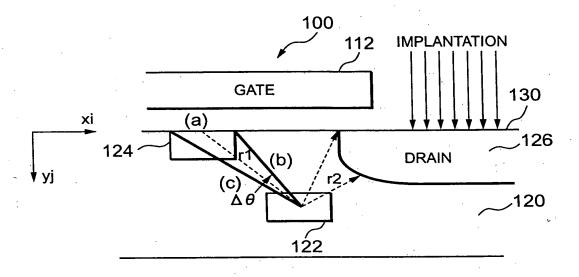


Fig.5

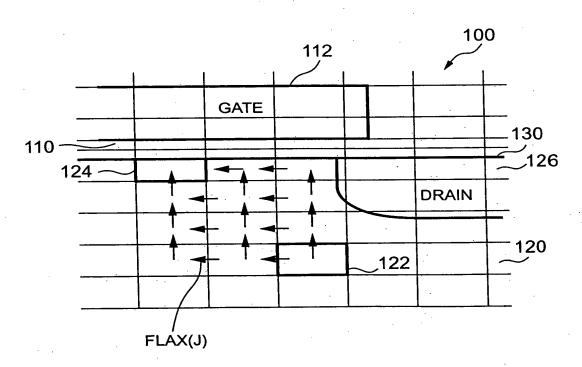


Fig.6

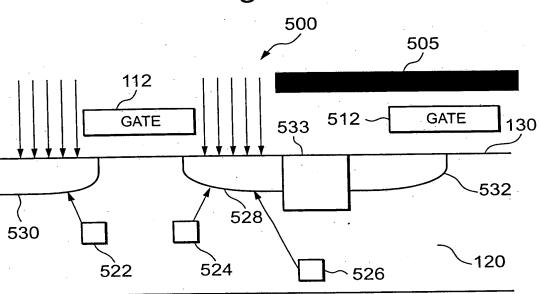


Fig.7

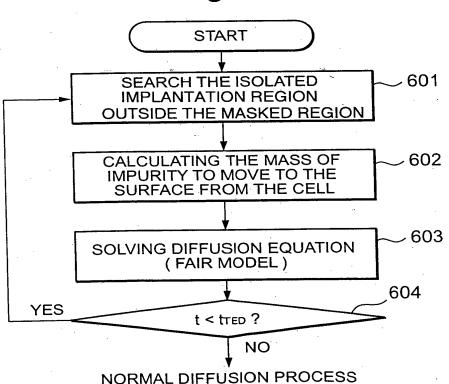


Fig.8

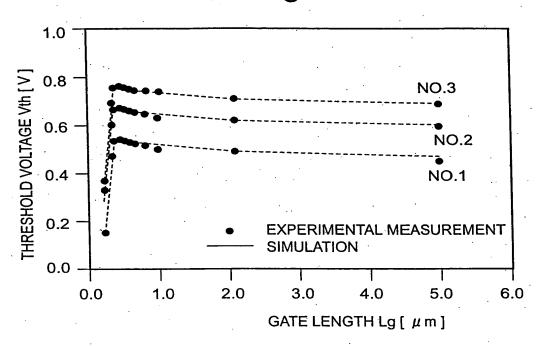


Fig.9

PRIOR ART

VIIV

VIIV

AI → A+I

AV → A+V

VIIV

I

A+I → AI

A+V → AV

Fig. 10A PRIOR ART

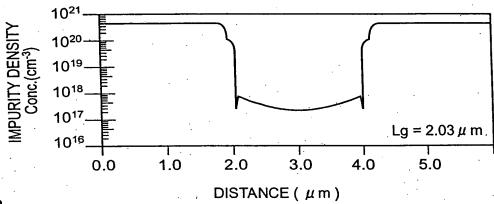


Fig. 10B PRIOR ART

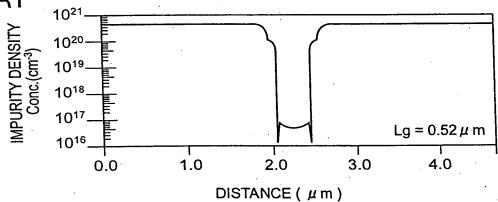


Fig. 10C PRIOR ART

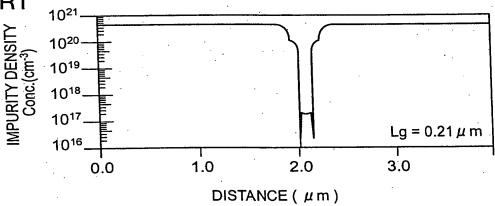


Fig.11 PRIOR ART

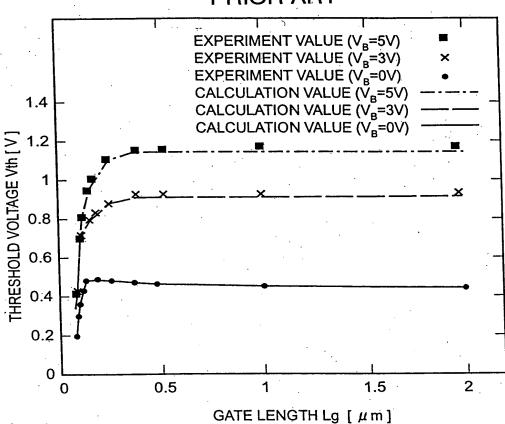


Fig.12 PRIOR ART

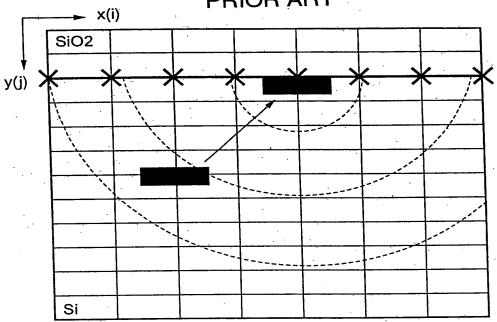


Fig.13 PRIOR ART

